Extending enumerative function synthesis via SMT-driven classification

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 $UF \underline{m}G$

THE UNIVERSITY OF IOWA

Lógicos em Quarentena 2020-04-30, The Internet





In	Out	P(x) =
0	1	
1	2	
2	4	
3	8	



In	Out	P(x) = ite(x < 2, x + 1, ite(x < 3, 2 * x, 2 * x + 2))
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1	2	
2	4	
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In	Out	P(x) = ite(x < 2, x + 1, ite(x < 3, 2 * x, 2 * x + 2))
0	1	$P(x) = 2^x$
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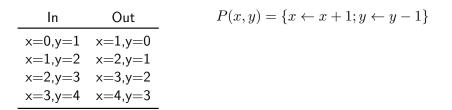


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0	1	$P(x) = 2^x$
1	2	$P(x) = \dots$
2	4	$\Gamma(\omega) = \cdots$
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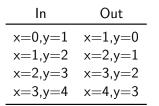


In	Out
x=0,y=1	x=1,y=0
x=1,y=2	x=2,y=1
x=2,y=3	x=3,y=2
x=3,y=4	x=4,y=3









$$P(x, y) = \{x \leftarrow x + 1; y \leftarrow y - 1\}$$
$$P(x, y) = \{z \leftarrow x; x \leftarrow y; y \leftarrow z\}$$



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$$P(x, y) = \dots$$



- Main challenges:
 - Exploring search space
 - Capturing intention

$$\begin{array}{c|ccc} In & Out \\ \hline x=0,y=1 & x=1,y=0 \\ x=1,y=2 & x=2,y=1 \\ x=2,y=3 & x=3,y=2 \\ x=3,y=4 & x=4,y=3 \end{array}$$

- ▷ Three main characteristics
 - How to write specification
 - How to constrain search space
 - How to guide the search

$$P(x, y) = \{x \leftarrow x + 1; y \leftarrow y - 1\}$$
$$P(x, y) = \{z \leftarrow x; x \leftarrow y; y \leftarrow z\}$$
$$P(x, y) = \dots$$

Some applications of program synthesis

Superoptimization [SSA13], [NRB+19], ... ▷ Program repair [NWK+17], [LCL+17], ... \triangleright Programming by examples [Gul11], [FMG+17] ... \triangleright Circuit synthesis [EWW16], ... ▷ Loop invariant synthesis [GLM+14], [PSM16], ...

▷ ...



[ABJ+13]

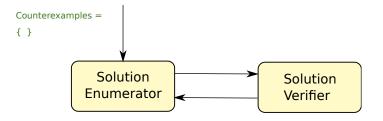
 \triangleright Specification is given by (second-order) \mathcal{T} -formula: $\exists f. \forall \bar{x}. \varphi[f, \bar{x}]$

 $\,\triangleright\,$ Syntactic restrictions given by context-free grammar R

Consider the example:

$$\varphi = f(x, x) \simeq x + 1 \land f(x, x + 1) \simeq x$$

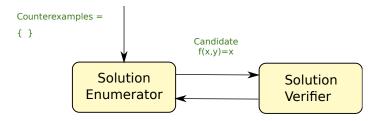
$$R = A \rightarrow 0 \mid 1 \mid x \mid y \mid A + A \mid \text{ite}(B, A, A)$$
$$B \rightarrow A \leq A \mid \neg B$$



 $\,\triangleright\,$ De facto approach to SyGuS solving given its simplicity and efficacy

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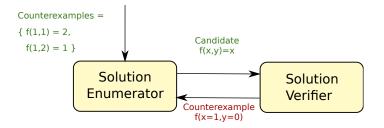
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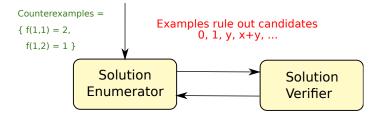
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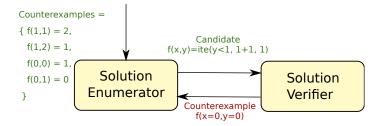
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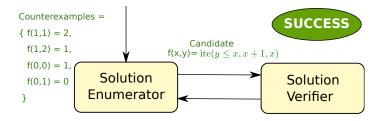
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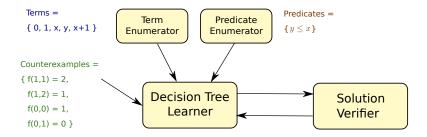
▷ De facto approach to SyGuS solving given its simplicity and efficacy

Enumerative techniques are effective but limited by the explosion of the enumeration space as term size increases

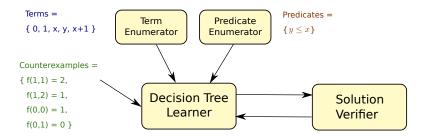
For this bit-vector grammar, enumerating

- \triangleright Terms of size = 1 : .05 seconds
- \triangleright Terms of size = 2 : .6 seconds
- \triangleright Terms of size = 3 : 48 seconds
- \triangleright Terms of size = 4 : 5.8 hours
- \triangleright Terms of size = 5 : ??? (100+ days)

```
(synth-fun f ((s (BitVec 4))
        (t (BitVec 4)))
(BitVec 4) (
(Start (BitVec 4) (
    s t #x0
    (bvneg Start)
    (bvnd Start)
    (bvadd Start Start)
    (bvadd Start Start)
    (bvald Start Start)
    (bvlshr Start Start)
    (bvor Start Start)
    (bvor Start Start)
    (bvor Start Start))
))
```

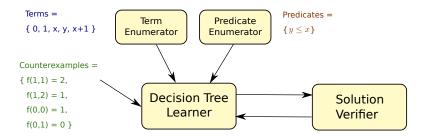


 $\,\vartriangleright\,$ Generate partial solutions correct on subset of input



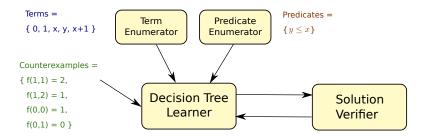
 \triangleright Generate partial solutions correct on subset of input

$$\underbrace{ \{f(1,1) = 2, f(0,0) = 1\}}_{\{f(1,2) = 1, f(0,1) = 0\}}$$



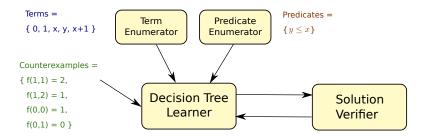
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$$\begin{array}{c} & & & & & \\ T & & & & & \\ f(1,1) = 2, f(0,0) = 1 \\ 0 \\ & & \\ 0 \\ \end{array} \begin{array}{c} & & \\ \end{array} \begin{array}{c} & & \\ \end{array} \begin{array}{c} & & \\ & & \\ \end{array} \begin{array}{c} & & \\ \end{array} \end{array}{} \end{array}$$

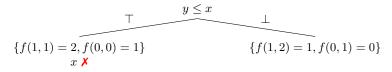


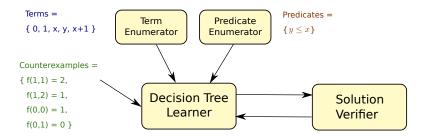
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$$\begin{array}{c} & & & & & \\ T & & & & & \\ f(1,1) = 2, f(0,0) = 1 \\ 1 \\ & & \\ 1 \\ \end{array} \begin{array}{c} & & \\ \end{array} \begin{array}{c} & & \\ \end{array} \begin{array}{c} & & \\ & & \\ \end{array} \begin{array}{c} & & \\ & & \\ \end{array} \begin{array}{c} & & \\ \end{array} \end{array}$$

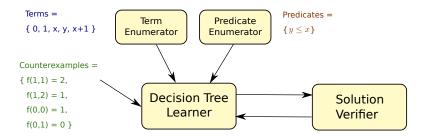


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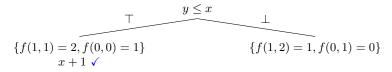


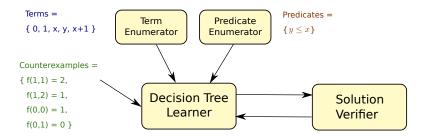


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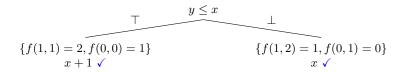


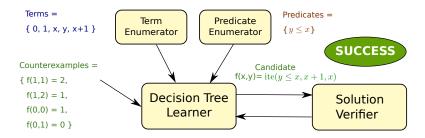
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Generate partial solutions correct on subset of input
 Unify partial solutions via decision tree learning





Generate partial solutions correct on subset of input
 Unify partial solutions via decision tree learning

$$\begin{array}{c} & & & & & \\ & & & & \\ f(1,1)=2, f(0,0)=1 \} & & & \\ & & & & \\ x+1 \checkmark & & & \\ \end{array} \begin{array}{c} & & & & \\ f(1,2)=1, f(0,1)=0 \} \\ & & & & \\ x \checkmark \end{array}$$

▷ D&C provides much better scalability

However...

- ▷ D&C can only be applied to point-wise specifications
 - Each input valuation is specified independently

However...

D&C can only be applied to point-wise specifications
 Each input valuation is specified independently

Consider augmenting the previous example:

$$\begin{array}{ll} \varphi = & f(x,\,x)\simeq x+1\,\wedge\,f(x,\,x+1)\simeq x\\ & \wedge & f(x,\,y)\simeq x+1 \Rightarrow f(x+2,\,y)\simeq x \end{array}$$

Counterexample $\{x \mapsto 1, y \mapsto 0\}$ yields the constraints:

 $f(1,\,1)\simeq 2 \quad \wedge \quad f(1,\,2)\simeq 1 \quad \wedge \quad f(1,\,0)\simeq 2 \Rightarrow f(3,\,0)\simeq 1$

 \triangleright A solution for f(1, 0) restricts the solution for f(3, 0)

> Breaks assumption that partial solutions can be found *indepedently*

Challenges

. . .

> This limitation excludes interesting classes of synthesis problems

- ▶ Invariants: $I(x) \land T(x, x') \Rightarrow I(x')$
- Ranking functions: rank(x') < rank(x)
- Modular arithmetic functions: $f(x) \simeq f(x+n)$
- ▷ Extending D&C to arbitrary (non-point-wise) specifications:
 - ▶ Find a term assignment consistent with point dependencies

Correctly classify points according to term assignment

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SMT solving

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SMT solving for SyGuS

Satisfiability Modulo Theories (SMT)

First-order formulas $t ::= x \mid f(t, \dots, t)$ in CNF: $\varphi ::= p(t, \dots, t) \mid \neg \varphi \mid \varphi \lor \varphi \mid \forall x_1 \dots x_n, \varphi$

Given a formula φ in FOL and background theories $\mathcal{T}_1, \ldots, \mathcal{T}_n$, finding a model \mathcal{M} giving an *interpretation* to all terms and predicates such that $\mathcal{M} \models_{\mathcal{T}_1,\ldots,\mathcal{T}_n} \varphi$

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Example

Is φ satisfiable modulo equality and arithmetic?

 $\varphi \hspace{.1in} = \hspace{.1in} (x_1 \geq 0) \wedge (x_1 < 1) \hspace{.1in} \wedge \hspace{.1in} (f(x_1) \not\simeq f(0) \hspace{.1in} \vee \hspace{.1in} x_3 + x_1 > x_3 + 1)$

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$$\varphi = \underbrace{(x_1 \ge 0) \land (x_1 < 1)}_{\text{LIA}} \land \underbrace{(f(x_1) \not\simeq f(0))}_{\text{EUF}} \lor \underbrace{x_3 + x_1 > x_3 + 1}_{\text{LIA}})$$

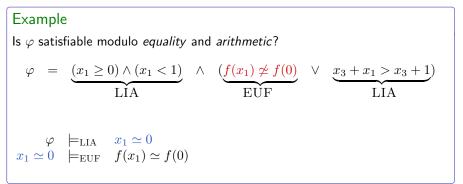
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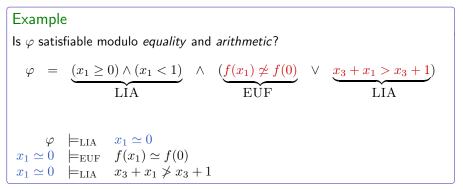
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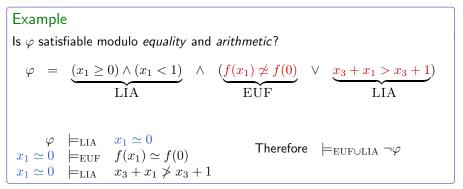
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SMT solving

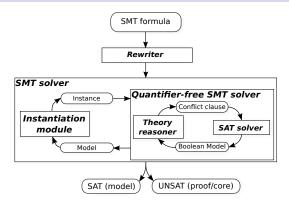
...

- Decidability depends on the theories being used
- Efficient decision procedures
 - ► Equality and uninterpreted functions (Congruence Closure (CC))
 - [NO80], [DST80]

[DM06]

- Algebraic datatypes (CC + Injectivity, Distinctness, Exhaustiveness, Acyclicity) [BST07]
- Linear arithmetic (Simplex)
- Bit-vectors (Bit-blasting)
- Combination of theories (Nelson-Oppen)
- ▷ Boolean search leverages SAT solvers
- ▷ Users may define their own theories
 - New operators as uninterpreted functions + Axioms

$\mathsf{CDCL}(\mathcal{T})$ architecture



▷ Rewriter simplifies terms

 $x + 0 \to x \qquad \mathsf{a} \not\simeq \mathsf{a} \to \bot \qquad (\text{str.replace } x \ (\text{str.}{+}{+}\ x \ x) \ y) \to x$

- > SAT solver enumerates models for Boolean skeleton of formula
- \triangleright Theory solvers check consistency in the theory
- Instantiation module selects relevant instances

▷ Encode problem using a deep embedding into datatypes

$$\varphi = f(x, x) \simeq x + 1 \land f(x, x + 1) \simeq x$$

$$R = A \rightarrow 0 \mid 1 \mid x \mid y \mid A + A \mid \text{ite}(B, A, A)$$
$$B \rightarrow A \le A \mid \neg B$$

Becomes

$$[\![\varphi]\!] = \operatorname{eval}_{\mathbf{a}}(d, x, x) \simeq x + 1 \, \wedge \, \operatorname{eval}_{\mathbf{a}}(d, x, x + 1) \simeq x$$

$$[R] = a = Zero | One | X | Y | Plus(a, a) | Ite(b, a, a) b = Leq(a, a) | Neg(b)$$

▷ eval maps datatype terms to their corresponding theory terms
 ▶ eval_a(Plus(X, X), 2, 3) is interpreted as (x + x){x → 2, y → 3} = 4

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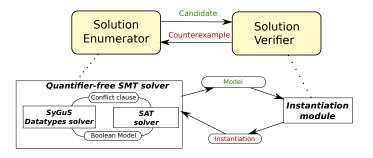
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- ▷ eval maps datatype terms to their corresponding theory terms
 ▶ eval_a(Plus(X, X), 2, 3) is interpreted as (x + x){x → 2, y → 3} = 4
- \triangleright A solution is a model in which e.g.



> An instantiation module checks candidates against the specification

- Generates lemmas witnessing why a candidate failed
- ▷ A specialized datatypes solver for SyGuS generates candidate solutions
 - Must satisfy all lemmas
 - Dedicated pruning
 - Parameterizable fairness criteria for enumeration

Unif+PI: a general divide-and-conquer framework for SyGuS solving

Recapping

 $\,\vartriangleright\,$ D&C can only be applied to point-wise specifications

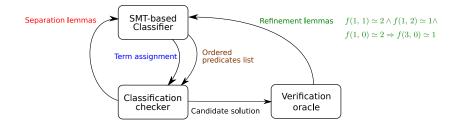
- Each input valuation is specified independently
- ▷ Extending D&C to arbitrary (non-point-wise) specifications requires:
 - Find a term assignment consistent with point dependencies

SMT solving

Correctly classify points according to term assignment

Decision tree learning

- SMT-based solution-complete strategy
- Heuristic strategy



▷ SMT-based classifier

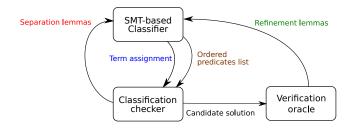
Assigns terms to points so that lemmas hold

 $f(1,\,1)\mapsto y+y,\quad \{f(1,\,0),\,f(3,\,0),\,f(1,\,2)\}\mapsto x$

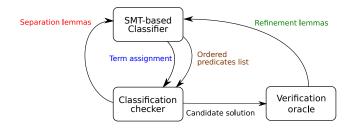
▶ Generates ordered list of predicates to *separate* points: $P_1 \mapsto x \neq y$

 Classification checker: whether corresponding decision tree correctly classifies sample

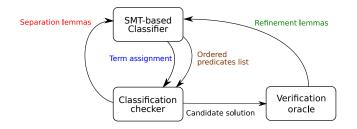
Failures are encoded as separation lemmas



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- Bounded solution-completeness and minimality results due to exhaustive enumeration of possible classifiers according to
 - size and number of distinct terms to be assigned
 - size and number of distinct predicates



- $\,\triangleright\,$ Successful candidates that are not verified lead to refinement lemmas and the learning restarts
- Bounded solution-completeness and minimality results due to exhaustive enumeration of possible classifiers according to
 - size and number of distinct terms to be assigned
 - size and number of distinct predicates
- \triangleright Our fairness criteria are $size = log_2(\#terms), \#pred = \#terms 1$

$$\begin{split} \varphi = & f(x,\,x) \simeq x + 1 \, \wedge \, f(x,\,x+1) \simeq x \\ & \wedge & f(x,\,y) \simeq x + 1 \Rightarrow f(x+2,\,y) \simeq x \end{split}$$

▷ Initially a single term of size 0 will be a trivial successful classifier

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▷ Refinement lemma:

 $f(1,\,1)\simeq 2 \quad \wedge \quad f(1,\,0)\simeq 2 \Rightarrow f(3,\,0)\simeq 1 \quad \wedge \quad f(1,\,2)\simeq 1$

$$\begin{split} \varphi = & f(x,\,x) \simeq x + 1 \, \wedge \, f(x,\,x+1) \simeq x \\ \wedge & f(x,\,y) \simeq x + 1 \Rightarrow f(x+2,\,y) \simeq x \end{split}$$

> Initially a single term of size 0 will be a trivial successful classifier

Refinement lemma:

▶ Maximum size increases to 1 and up to 1 predicate can be used

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▷ A candidate classifier is

 $\begin{array}{l} f(1,\,1)\mapsto y+y, \quad \{f(1,\,0),\,f(3,\,0),\,f(1,\,2)\}\mapsto x\\ P_1\mapsto\top \end{array}$

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 \vartriangleright This classifier fails on the sample, yielding a separation lemma $P_1\simeq \top \Rightarrow f(1,\,1)\simeq f(1,\,0)$

▷ Given this constraints and current threshold the next candidate classifier produced is:

 $\{ f(1, 1), f(1, 0), f(3, 0) \} \mapsto y + 1, \quad f(1, 2) \mapsto 1$ $P_1 \mapsto y \le x$

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f(1,1)

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▷ Running the classification checker:

 $f(1,1), f(1,0), f(3,0) \diamond f(1,2) \rightarrow \overbrace{f(1,1), f(1,0), f(3,0)}^{y \leq x} f(1,2)$

▷ Given this constraints and current threshold the next candidate classifier produced is:

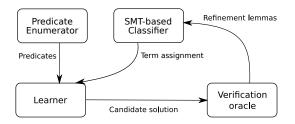
 $\{ f(1, 1), f(1, 0), f(3, 0) \} \mapsto y + 1, \quad f(1, 2) \mapsto 1 \\ P_1 \mapsto y \le x$

▷ Running the classification checker:

$$f(1,1), f(1,0), f(3,0) \diamond f(1,2) \rightarrow \overbrace{f(1,1), f(1,0), f(3,0)}^{\forall \ y \le x} f(1,2)$$

- \triangleright As the classification succeeds, a candidate is generated
- The candidate fails, so the process restarts with new refinement lemmas
- \triangleright Eventually finds solution $f = \lambda xy$. ite $(x \le y, \text{ ite}(y \le x, x + 1, x), y)$

Unif+PI with unconstrained predicate enumeration



▷ Unif+PI+E uses SMT solver only to produce term assignments

- Relies on standard decision tree learning to classify a labeled sample
- > Predicates chosen from enumerated pool with information-gain heuristic
- Separation conflicts solved when new predicates are enumerated
- Often sacrificing completeness and minimality allows problems to be solved more efficiently

Experimental results

Setup

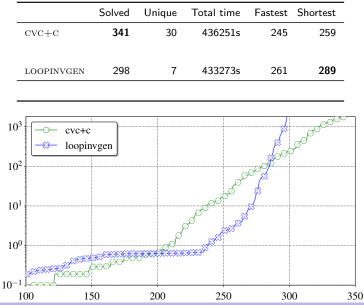
- ▷ Benchmarks (all over LIA)
 - ▶ 127 invariant synthesis benchmarks from SyGuS-COMP'18
 - ▶ 440 invariant synthesis benchmarks from test suite of Kind 2
- $\triangleright~$ Three configurations of $_{\rm CVC4SY}$

CVC+C	enumerative CEGIS [RBN+19]				
CVC+UPI	Unif+PI				
CVC+UPI+E	Unif+PI+E				

- ▷ LOOPINVGEN [PM17] and CVC+C as baselines
- ▷ 1800s timeout, 8gb RAM

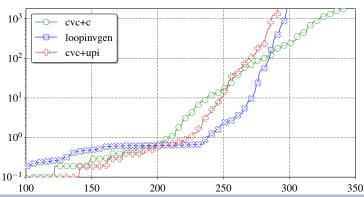
Full data at http://cvc4.cs.stanford.edu/papers/FMCAD2019-UnifPI/

Summary



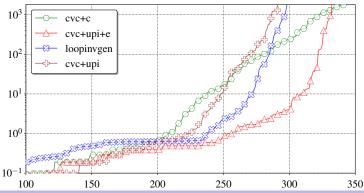
Summary

	Solved	Unique	Total time	Fastest	Shortest
CVC+C	341	30	436251s	245	259
CVC+UPI LOOPINVGEN	291 298	3 7	494534s 433273s	236 261	231 289



Summary

	Solved	Unique	Total time	Fastest	Shortest
CVC+C	341	30	436251s	245	259
CVC+UPI+E	332	47	414356 s	306	222
CVC+UPI	291	3	494534s	236	231
LOOPINVGEN	298	7	433273s	261	289
CVC-PORT	400	-	31476s	379	306



Advantages and disadvantages of Unif+PI

- CVC+UPI and CVC+UPI+E thrive when invariants can be built from combination of small literals
- $\,\triangleright\,$ $_{\rm CVC+C}$ is superior when invariant is a single complex literal
 - > 29 of its 30 unique solves are such cases

- \vartriangleright $_{\rm CVC+UPI}$ and $_{\rm CVC+UPI+E}$ also suffer from dependence on samples
 - Sometimes search is biased towards simple classifiers when only a more complex one would suffice

SyGuS-COMP 2019

Inv Track (829)

Solver	Solved	Fastest	Smallest	Score
CVC4-su	592	423	264	4493
LoopInvGen	512	442	364	4250
LoopInvGen-gplearn	511	411	349	4137
CVC4-Fast	522	319	243	3810
CVC4-Smart	539	283	260	3804
OASIS	538	20	317	3067
DryadSynth	277	161	39	1907





829 benchmarks from the literature in loop invariant synthesis
 3600s timeout

Extending enumerative function synthesis via SMT-driven classification

Injecting some welcome realism

- \triangleright Kind 2 employs in cooperation:
 - IC3 [Bra11]
 k-induction [SSS00]
 Generation of auxiliary invariants [KGT11]
- \triangleright Kind 2 solves all the 480 benchmarks it its test suite in less than 120s
- \triangleright Considering k-induction in isolation, CVC-PORT is competitive

	Solved	Unique	Time (commonly solved)
CVC-PORT	323	82	109.6
Kind 2 (k-induction)	313	72	9.6

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	Solved	Unique	Time (commonly solved)
CVC-PORT	323	82	109.6
Kind 2 (k-induction)		72	9.6

- $\,\triangleright\,$ We consider this encouraging given our framework is
 - not theory-specific
 - single-threaded
 - not optimized for reachability

Conclusions

\triangleright New enumerative function synthesis framework via divide and conquer

- No dependence on point-wise specifications
- Powered by SMT-driven classification algorithms
- Implemented in CVC4SY

Experimental evaluation shows significant gains w.r.t. previous SyGuS techniques for invariant synthesis

Future work

- Improving classification
 - ▶ Using constraint solving for synthesizing term assignments
 - ▶ Only considering relevant arguments when synthesizing predicates $f(0, 0, 0, 1, 2, 1, 0) \diamond f(1, 0, 0, 5, 2, 1, 3)$
 - Can drastically reduce search space
- Improving sample
 - Reducing noise: make points as similar as possible

 $f(1, 0, 0, 1, 2, 1, 0) \diamond f(1, 0, 0, 5, 2, 1, 0)$

Improve diversity via clustering analysis: only add new points to sample that are sufficiently different Extending enumerative function synthesis via SMT-driven classification

<u>Haniel Barbosa</u>, Andrew Reynolds, Daniel Larraz, Cesare Tinelli

 $UF \underline{m}G$

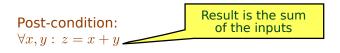
THE UNIVERSITY OF IOWA

Lógicos em Quarentena 2020-04-30, The Internet

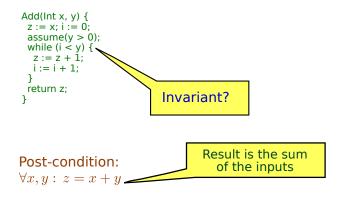
Extra slides

Invariant Synthesis

```
Add(Int x, y) {
z := x; i := 0;
assume(y > 0);
while (i < y) {
z := z + 1;
i := i + 1;
}
return z;
}
```

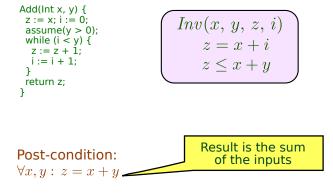


Invariant Synthesis



Verification:

Invariant Synthesis



Verification:

- State-of-the-art: LoopInvGen [PM17]: data-driven loop invariant inference with automatic feature synthesis
 - ▶ Precondition inference from sets of "good" and "bad" states
 - Feature synthesis for solving conflicts
 - PAC (probably approximately correct) algorithm for building candidate invariants
- "Bad" states are dependent on model of initial condition (no guaranteed convergence)
- ▷ No support for implication counterexamples

Invariant Synthesis with Unif+PI

- ▷ Refinement lemmas allows derivation of three kinds on data points:
 - "good points" (invariant must always hold)
 - "bad points" (invariant can never hold)
 - "implication points" (if invariant holds in first point it must hold in second)
- ▷ Native support for implication counterexamples
- Straightforward usage of classic information gain heuristic to build candidate solutions with decision tree learning
 - SMT solver "resolves" implication counterexample points as "good" and "bad"
 - Out-of-the-box Shannon entropy



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